

Figure 1. Transfer curve with negative offset

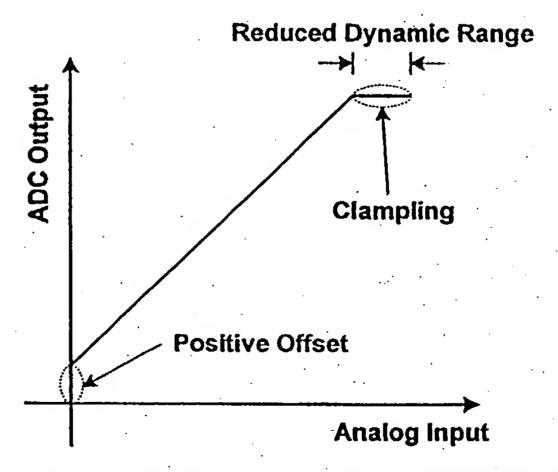


Figure 2. Transfer curve with positive offset

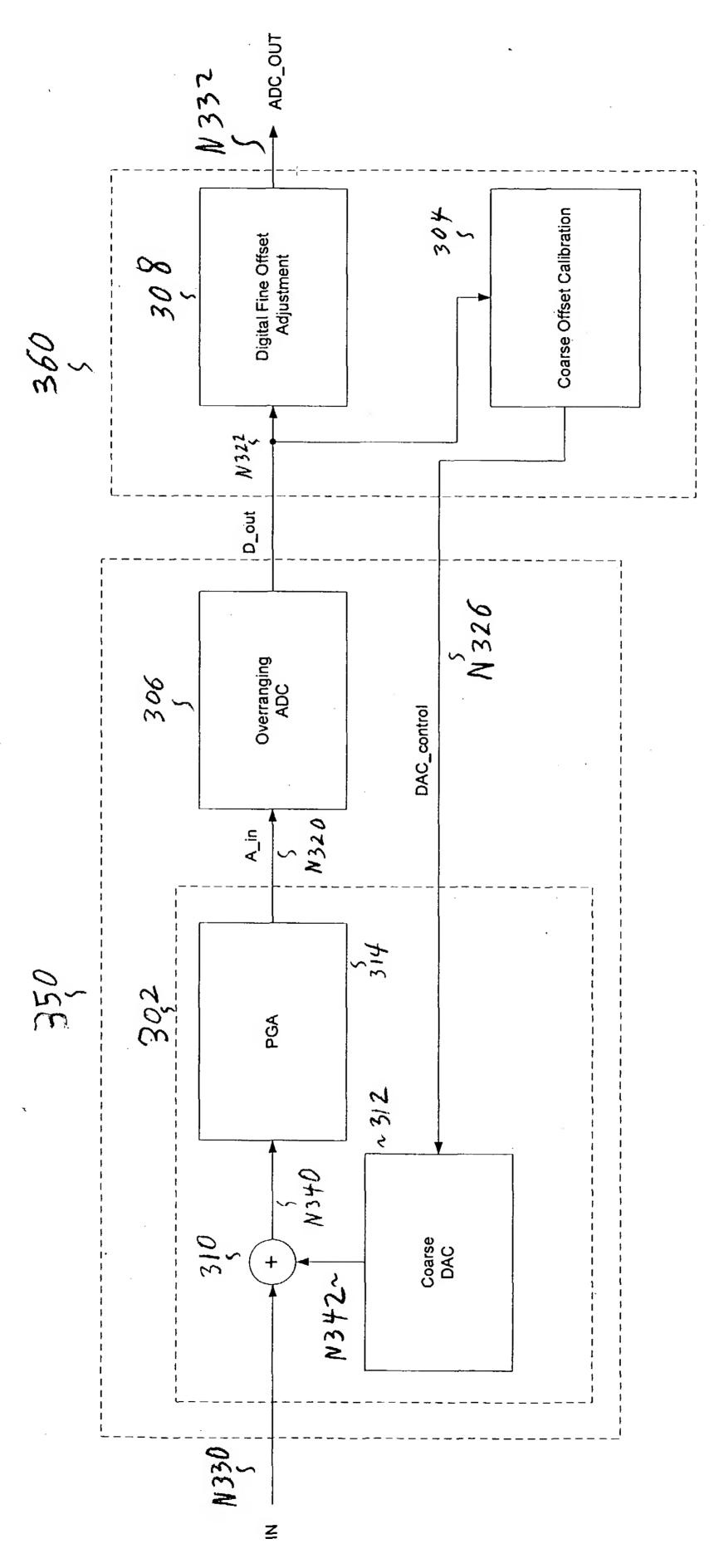


Figure 3

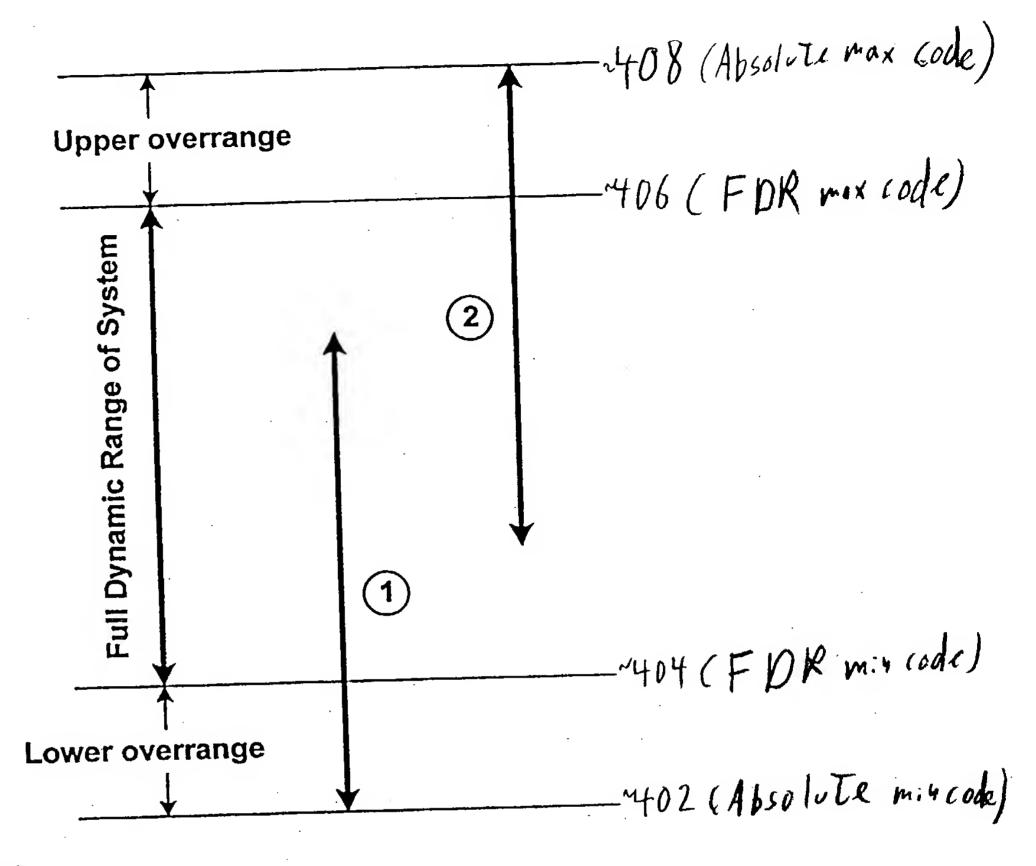


Figure 4.

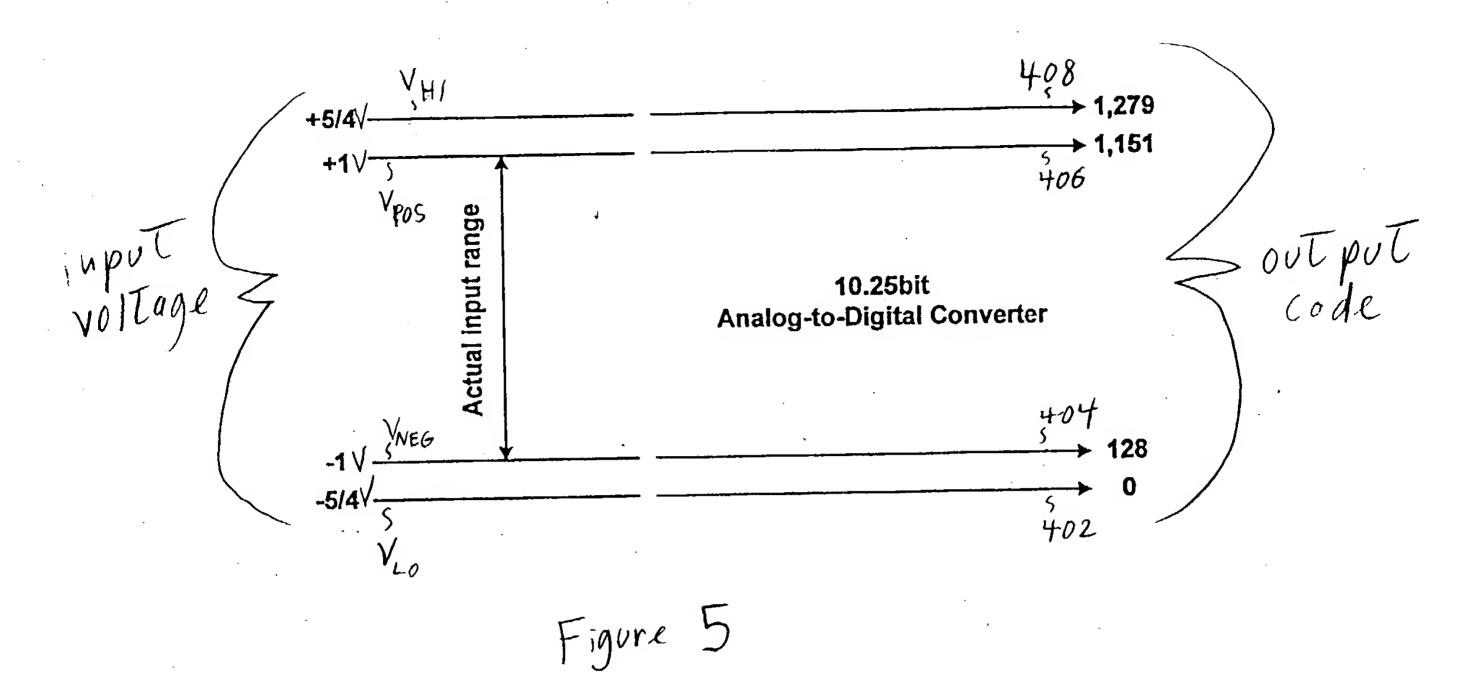
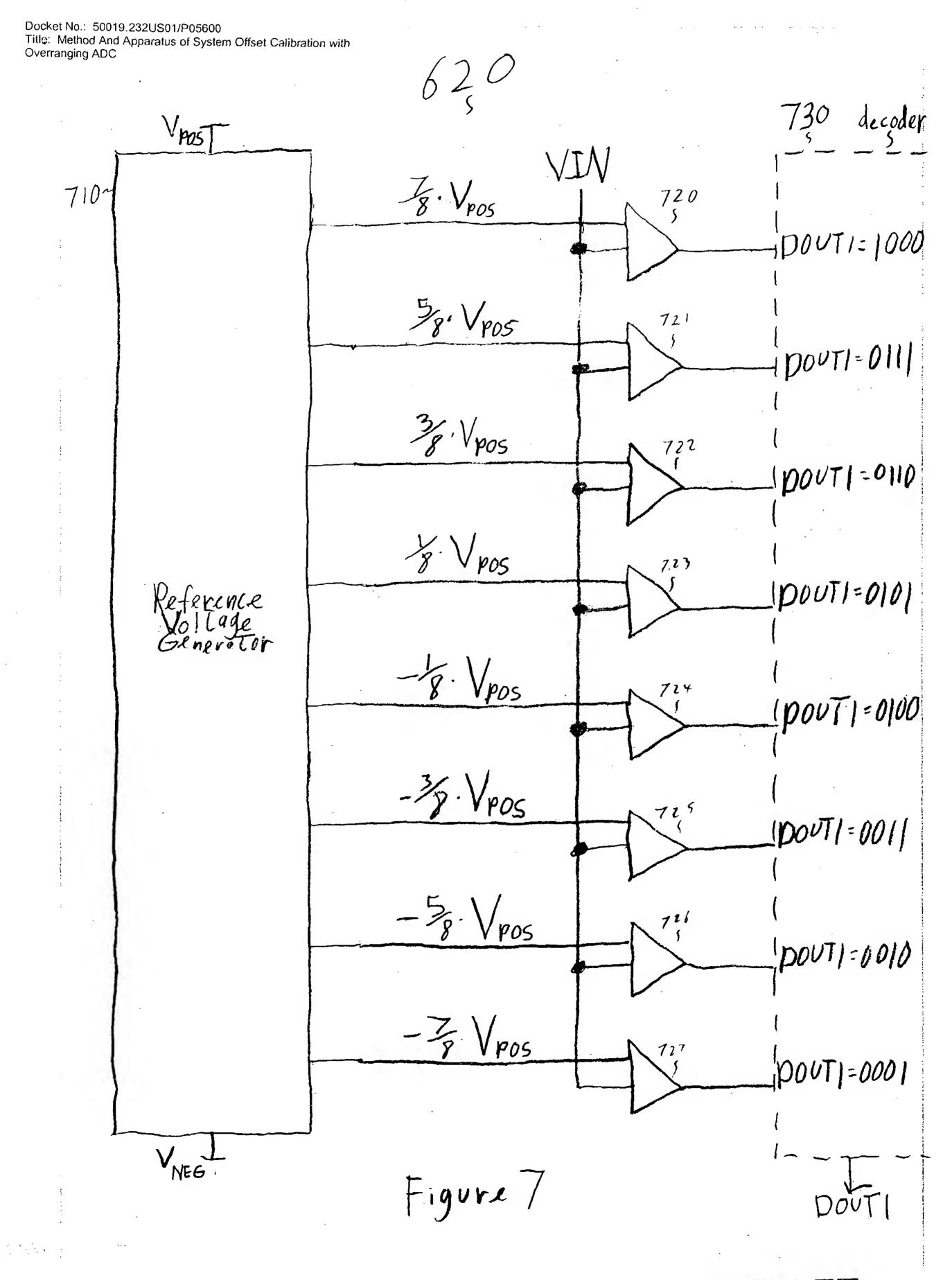


Figure 6

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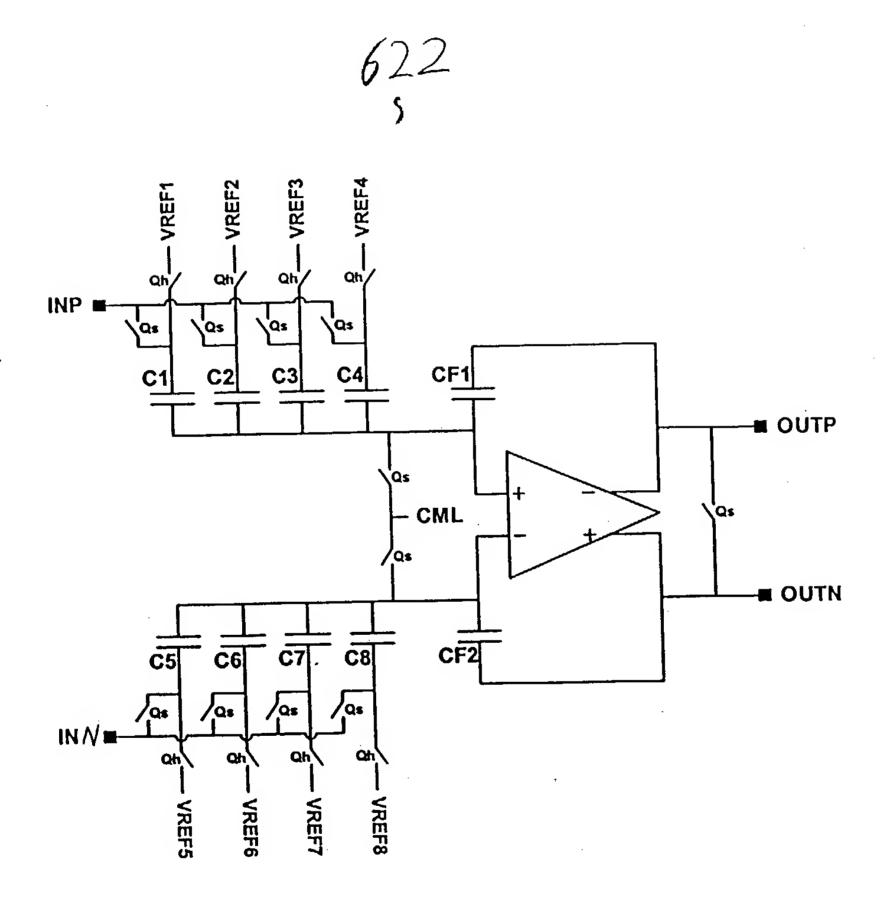
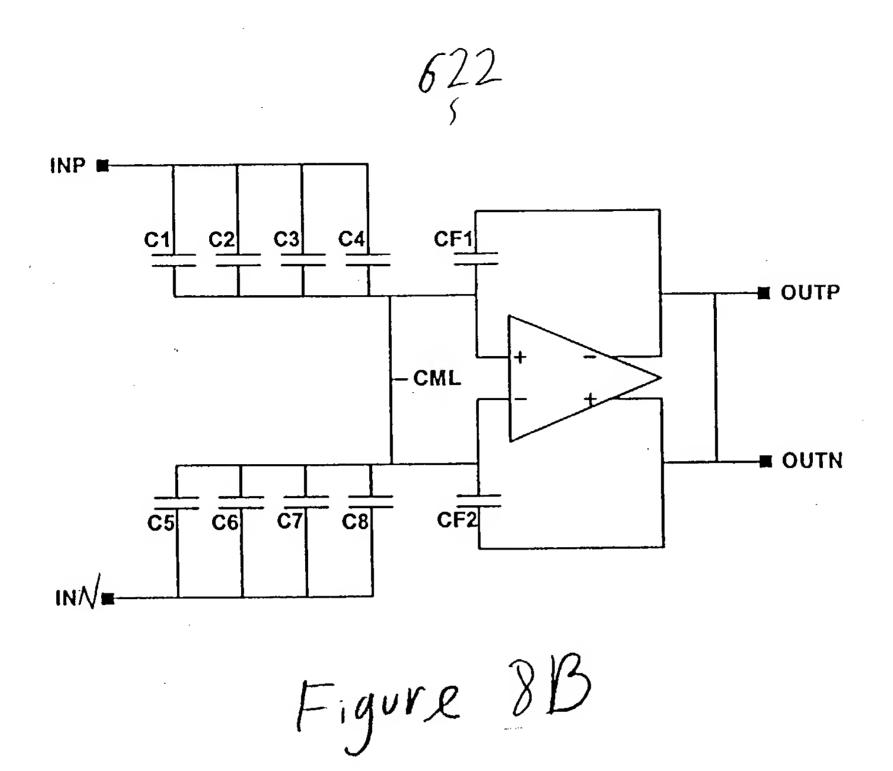


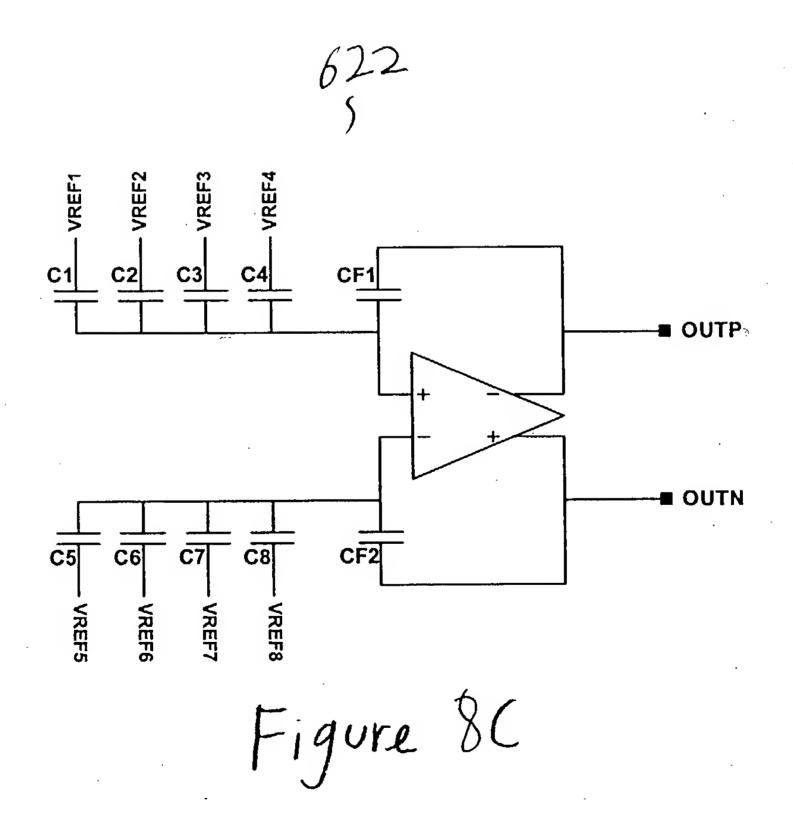
Figure 8A



Docket No.: 50019.232US01/P05600

Title: Method And Apparatus of System Offset Calibration with

Overranging ADC



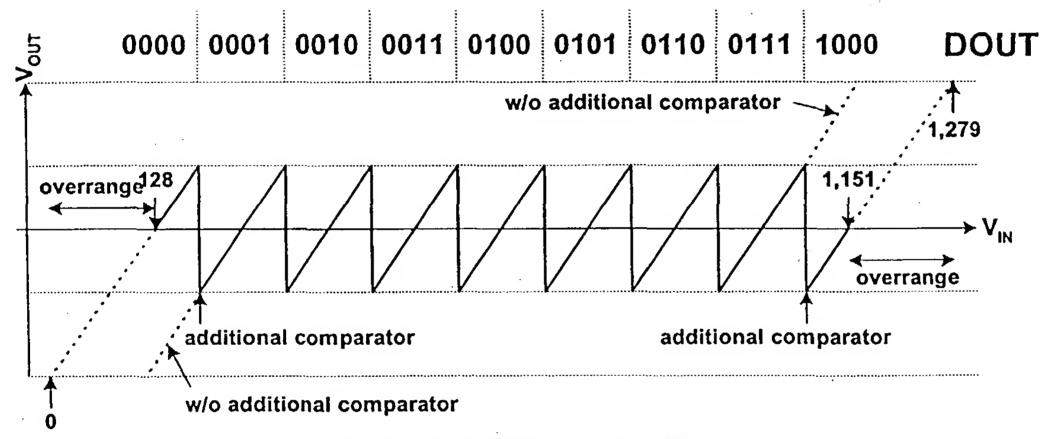
	VREF1	VREF2	VREF3	VREF4	VREF5	VREF6	VREF7	VREF8	V _{out}	LPOUTI
7/8 <vin< td=""><td>0.5</td><td>0.5</td><td>0.5</td><td>0.5</td><td>-0.5</td><td>-0.5</td><td>-0.5</td><td>-0.5</td><td>4V_{IN}- 4</td><td>1000</td></vin<>	0.5	0.5	0.5	0.5	-0.5	-0.5	-0.5	-0.5	4V _{IN} - 4	1000
5/8 <vin< 7="" 8<="" td=""><td>0.5</td><td>0.5</td><td>0.5</td><td>0</td><td>. 0</td><td>-0.5</td><td>-0.5</td><td>-0.5</td><td>4V_{IN}- 3</td><td>0111</td></vin<>	0.5	0.5	0.5	0	. 0	-0.5	-0.5	-0.5	4V _{IN} - 3	0111
3/8 <vin< 5="" 8<="" td=""><td>0.5</td><td>0.5</td><td>0</td><td>0</td><td>0</td><td>0</td><td>-0.5</td><td>-0.5</td><td>4V_{IN}- 2</td><td>0110</td></vin<>	0.5	0.5	0	0	0	0	-0.5	-0.5	4V _{IN} - 2	0110
1/8 <vin< 3="" 8<="" td=""><td>0.5</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>-0.5</td><td>4V_{IN}- 1</td><td>10101</td></vin<>	0.5	0	0	0	0	0	0	-0.5	4V _{IN} - 1	10101
-1/8 <vin< 1="" 8<="" td=""><td>0</td><td>0</td><td>O</td><td>0</td><td>0</td><td>0 -</td><td>0</td><td>0</td><td>4V_{IN}</td><td>10100</td></vin<>	0	0	O	0	0	0 -	0	0	4V _{IN}	10100
-1/8 <vin<-3 8<="" td=""><td>-0.5</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0.5</td><td>4V_{IN}+ 1</td><td>10011</td></vin<-3>	-0.5	0	0	0	0	0	0	0.5	4V _{IN} + 1	10011
-3/8 <vin<-5 8<="" td=""><td>-0.5</td><td>-0.5</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0.5</td><td>0.5</td><td>4V_{IN}+ 2</td><td>0010</td></vin<-5>	-0.5	-0.5	0	0	0	0	0.5	0.5	4V _{IN} + 2	0010
-7/8 <vin<-5 8<="" td=""><td>-0.5</td><td>-0.5</td><td>-0.5</td><td>0</td><td>0</td><td>0.5</td><td>0.5</td><td>0.5</td><td>4V_{IN}+ 3</td><td>10001</td></vin<-5>	-0.5	-0.5	-0.5	0	0	0.5	0.5	0.5	4V _{IN} + 3	10001
VIN<-7/8	-0.5	-0.5	-0.5	-0.5	0.5	0.5	0.5	0.5	4V _{IN} + 4	10000

Figure 9

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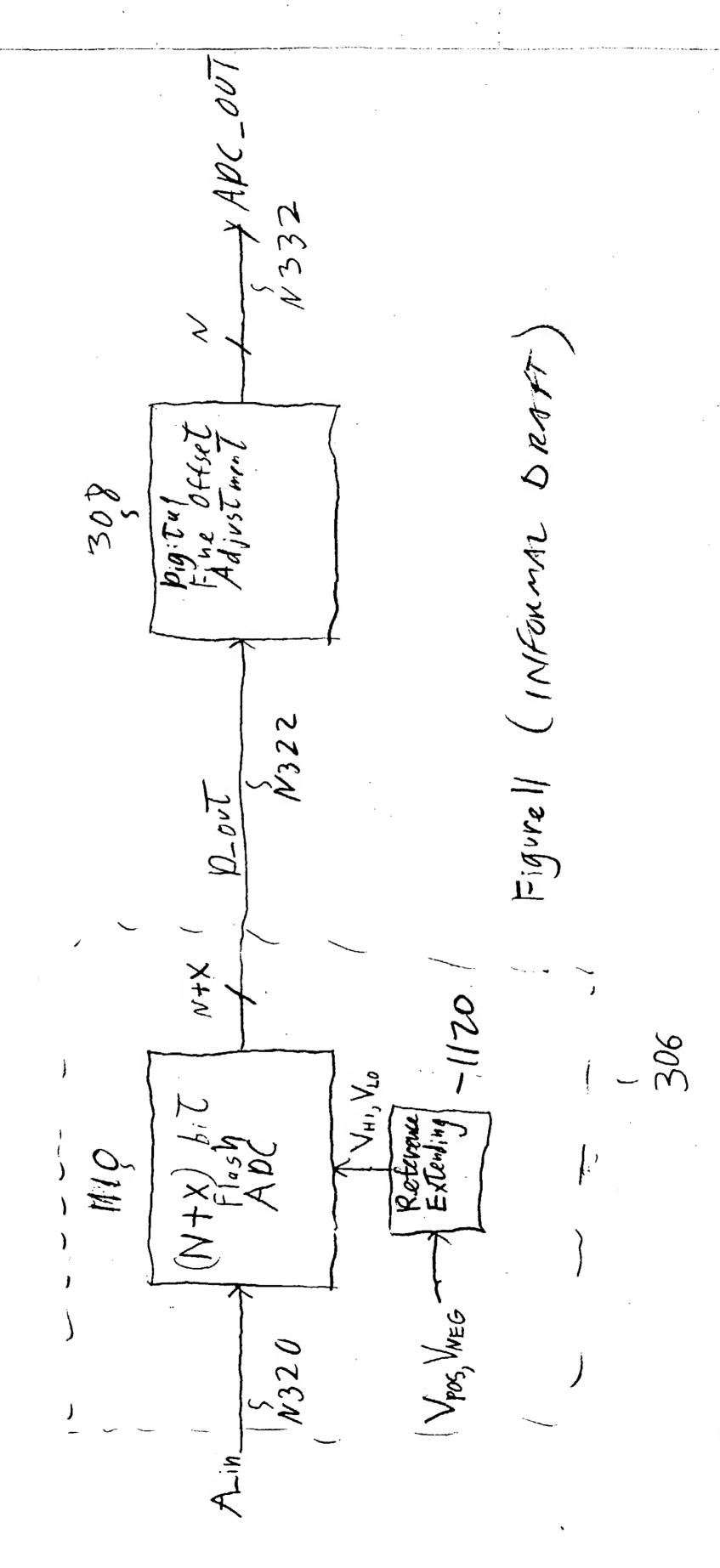


Residue of 3.5bit First MDAC

Figure 10



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